

Notice of References Cited

Application/Control No.

09/516,318

Applicant(s)/Patent Under

Reexamination

SISKA, CHARLES PAUL

Examiner

William H. Wood

Art Unit

2124

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,385,757	05-2002	Gupta et al.	716/1
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ramsey et al., "Specifying Representations of Machine Instructions", ACM Transactions on Programming Languages and Systems, Vol. 19, No. 3, May 1997, pages 492-524.
	V	Pees et al., "Retargetable Compiled Simulation of Embedded Processors Using a Machine Description Language", ACM Transactions on Design Automation of Electronic Systems, Vol. 5, No. 4, October 2000, page 815-834.
	W	Davidson et al., "Design Principles for Machine-Description Language", pages 1-21.
	X	Siska, Chuck. "A Processor Description Language Supporting Retargetable Multi-Pipeline DSP Program Development Tools".

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Russo et al., "Automatic Architecture Evaluation for Hardware/Software Codesign", IEEE, 1999, pages 47-53.
	V	Knoll et al., "Automated Generation of DSP Program Development Tools Using a Machine Description Formalism+", IEEE, 1993, pages I-457 to I-460.
	W	Zhu et al., "Retargetable Binary Utilities", DAC 2002, ACM, 2002, pages 331-336.
	X	Chung et al., "Fast System Development Tools Prototyping Using a Machine Instruction Set Description Language".

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.